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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

| | |
|---|--------------------|
| Docket Number (Optional) BUR9-2000-002 S1 | Application Number |
| Applicant(s) T. Furukawa et al. | |
| Filing Date | Group Art Unit |

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | REF | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---------------------|-----|-----------------|----------|--------------|-------|----------|-------------------------------|
| <i>72</i> | AA | 5,750,430 | 05/12/98 | Son | 438 | 303 | 08/23/96 |
| <i>72</i> | AB | 5,937,319 | 10/10/99 | Xiang et al. | 438 | 585 | 10/31/97 |
| <i>72</i> | AC | 5,981,383 | 11/09/99 | Lur et al. | 438 | 655 | 03/11/97 |
| <i>72</i> | AD | 6,015,724 | 01/18/00 | Yamazaki | 438 | 151 | 11/04/96 |
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FOREIGN PATENT DOCUMENTS

| REF | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | Translation | |
|-----|-----------------|------|---------|-------|----------|-------------|----|
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | |
|-----------|----|---|
| <i>72</i> | AE | "100 nm Gate Length High Performance / Low Power CMOS Transistor Structure" T. Ghani et al., 1999 IEEE, 0-7803-5413-3/99, IEDM 99 pages 415-418 |
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| EXAMINER <i>Kevin Quinlan</i> | DATE CONSIDERED <i>11/4/2001</i> |
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.